

3BLE1		2025-2026			Semester II	
Time/Day	Monday	Tuesday	Wednesday	Thursday	Friday	
9:00 am	EE348 Engineering Electromagnetics (ENG 2003)		EE Lab (3001/3002)		EE3126 3rd Year Project (3001/3003)	
10:00 am	EE348 Engineering Electromagnetics (ENG 2003)		EE Lab (3001/3002)	CT420 Real Time Systems (CSB 1006, Computer Science)	EE3126 3rd Year Project (3001/3003)	
11:00 am	EE Labs (3001/3002)	EE342 Analogue Sytems Design (ENG 2001)			EE3126 3rd Year Project (3002/3003)	
12:00 pm	EE Labs (3001/3002)	EE342 Analogue Sytems Design (ENG 2001)			EE3126 3rd Year Project (3002/3003)	
1:00 pm			EE348 Engineering Electromagnetics (ENG 2001)	EE342 Analogue Systems Design (ENG 3035)		
2:00 pm		CT420 Real Time Systems LAB (CSB G009, Computer Science)				
3:00 pm		CT420 Real Time Systems LAB (CSB G009, Computer Science)	CT420 Real Time Systems (Tyndall Theatre)	EE448 Power Electronics (ENG 2035)		
4:00 pm	EE448 Power Electronics (ENG 2034)	EE448 Power Electronics (ENG 2002)				
5:00 pm						
6:00 pm						

Please check Canvas regularly for details of individual modules. All timetable slots are of one hour duration unless otherwise specified